

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 7,148,536 B2
Patent Issue Date December 12, 2006
Application Serial No. 10/728,977
Filing Date December 8, 2003
Assignee Micron Technology, Inc.
Inventorship Belford T. Coursey
Attorney's Docket No. MI22-2459
Title: Memory Circuitry and Method of Forming Memory Circuitry

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR PTO MISTAKE (37 C.F.R. 1.322(a))

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 7,148,536 B2, granted December 12, 2006, in accordance with the Certificate of Correction form attached hereto.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.


Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under 37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, is Form PTO-1050, being suitable for printing.

Respectfully submitted,

Dated: 7-18-07

By:


Mark S. Matkin
Reg. No. 32,268

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,148,536 B2
DATED : December 12, 2006
INVENTOR(S) : Coursey

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 3, line 31, please delete "7" after "illustrated".

Col. 6, line 39, please delete "lines;" after "digit" and insert --lines,--.

Page
1 of 1

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